ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit device includes a first data transfer line electrically connected to a first memory cell block, a second data transfer line electrically connected to a second memory cell block, a charge circuit which charges any one of the first and second data transfer lines, a first data store circuit, second and third data store circuits electrically connected to the first data store circuit, a charge/discharge circuit which charges or discharges a voltage node on the basis of the data held in the third data store circuit, a first connecting circuit which electrically connects the voltage node to any one of the first and second data transfer lines, a fourth data store circuit, and a second connecting circuit which electrically connects the fourth data store circuit to the voltage node.